

## AMENDMENTS TO THE CLAIMS

The following Listing of Claims will replace all prior versions and listings of claims in this application.

### LISTING OF CLAIMS

1. (Currently amended) An array arrangement ~~(100, 200)~~ comprising ~~at least one group (206)~~ two or more groups of associated electronic units ~~(101, 201)~~ and each group of associated electronic units comprising [[an]] one external trigger line and an addressing circuit via which an activation signal can be sequentially fed to the electronic units of the group, wherein the addressing circuit of the group contains the following components:
  - a) ~~driver units (110, 210)~~ that are each disposed adjacently to an electronic unit ~~(101, 201)~~ and connected to it, wherein every driver unit has at least one connection input and at least one connection output and is designed to receive a trigger signal applied to the connection input and, after receipt thereof, to deliver an activation signal for a certain time duration to the ~~associated~~ connected electronic unit, and also to pass the trigger signal to the connection output;
  - b) ~~connecting lines (112, 212)~~ that link the connection inputs and connection outputs of the driver units of the group (110, 210) serially to one ~~another~~ another;
  - c) a single external clock line connected to internal clock lines connected to each driver unit for clocking the trigger and activation signals; and
  - d) a read out line connected to each electronic unit of the group.
2. (Currently amended) [[An]] The array arrangement as claimed in of claim 1, characterized in that the driver units ~~(110, 210)~~ are connected to ~~additional lines, preferably to a clock line (111, 114; 211, 214) for transmitting a clock signal, to an enable line for controlling the time duration of the activation signal, and/or and~~ to at least one line for supplying at least one control voltage serving as an activation signal.

3. (Currently amended) ~~[[An]]~~ The array arrangement ~~as claimed in~~ of claim 1, characterized in that the electronic units ~~(101, 201)~~ are disposed two-dimensionally in a regular pattern.
4. (Currently amended) ~~[[An]]~~ The array arrangement ~~as claimed in~~ of claim 1, characterized in that it contains a plurality of equally large groups of associated electronic units ~~(206)~~ in which the electronic units of each group ~~(101, 201)~~ ~~are each~~ are disposed in a similar way.
5. (Currently amended) ~~[[An]]~~ The array arrangement ~~as claimed in~~ of claim 1, characterized in that the electronic units of a group are disposed linearly ~~(101)~~ or in block fashion ~~(201)~~.
6. (Currently amended) ~~[[An]]~~ The array arrangement ~~as claimed in~~ of claim 1, characterized in that the electronic units of a group ~~(206)~~ are ~~sensor elements~~ ~~(101, 201)~~, ~~in particular~~ radiation sensors, connected to a read-out line ~~(105, 205)~~.
7. (Currently amended) ~~[[An]]~~ The array arrangement ~~as claimed in~~ of claim 1, characterized in that the electronic units are active light radiators or light switches.
8. (Currently amended) ~~[[An]]~~ The array arrangement ~~as claimed in~~ of claim 1, characterized in that the driver units ~~(110, 210)~~ contain at least one shift register.
9. (Currently amended) ~~[[An]]~~ The array arrangement ~~as claimed in~~ of claim 1, characterized in that it is implemented as an integrated circuit, in particular in silicon technology.
10. ~~A radiation detector, in particular an~~ An X-ray detector, containing an array arrangement ~~(100, 200)~~ of sensor elements ~~(101, 201)~~ as electronic units, the array arrangement being configured as claimed in claim 1.

11. (Currently amended) A display device containing an array arrangement (~~100, 200~~) of active light radiators or light switches as electronic units, the array arrangement being configured as claimed in claim 1.

12. (New) The array arrangement of claim 1, wherein each driver unit is connected to a plurality of electronic units of the group.